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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent of : Atty. Docket No. 97-GR2-144  
Antonio DO BENTO VIEIRA :  
Patent No.: 6,825,551 B1 :  
Issue Date: November 30, 2004 :  
For: *METHOD FOR PACKAGING A SEMICONDUCTOR CHIP CONTAINING SENSORS AND RESULTING PACKAGE*

TRANSMITTAL LETTER

**Certificate  
JAN 10 2005  
of Correction**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Attached hereto is a Certificate of Correction to correct errors in the above-referenced Letters Patent.

All of the errors being corrected are attributable to the U.S. Patent and Trademark Office, so it is submitted that no fee is required. However, if it should be determined that a fee is required, the Commissioner is authorized to charge the fee to Deposit Account No. 50-1556.

Respectfully submitted,

Date: 12/28/04

By: Jose Gutman

Jose Gutman  
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**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: COMMISSIONER FOR PATENTS, P.O. BOX 1450, ALEXANDRIA, VA 22313-1450

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(Also Form PTO-1050)

## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 6,825,551 B1  
DATED : November 30, 2004  
INVENTOR(S) : Antonio DO BENTO VIEIRA

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Please make the following changes:

Column 8, line 62 from "opening," to -- opening in the die carrier, --.  
Column 10, line 18 from "opening," to -- opening in the die carrier, --.  
Column 10, line 32 from "opening and" to -- opening in the die carrier and --.

MAILING ADDRESS OF SENDER: USPTO Customer Number: 23334

PATENT NO. 6,825,551 B1

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JAN 11 2005